

IN THE SPECIFICATION

Page 1 lines 1-2, delete the title, and substitute therefore
Memory Controller for Controlling Memory Accesses Across
Networks in Distributed Shared Memory Processing Systems.

Page 1, after line 3, insert the following paragraph:

This application is a divisional of U.S. Patent
Application S/N 08/890,341 filed 10 Jul 1997 by Howard T.
Olnowich for Memory Controller for Controlling Memory
Accesses Across Networks in Distributed Shared Memory
Processing Systems (as amended)

Page 1, lines 4-5, delete in their entirety and replace with
U. S. patent application Serial No. 08/891,404, filed 10
July 1997, entitled "Cache Coherent Network Adapter--.

Page 1, lines 6-7, delete ", assignee docket EN997036,".

Page 1, line 2, replace "FIG. 1 is" with --FIGS. 1A and 1B,
arranged as shown in FIG. 1, are--.

Page 13, line 14, replace "FIGS. 11A and 11B" with --FIGS.
11A, 11B and 11C--.

EN9997080B